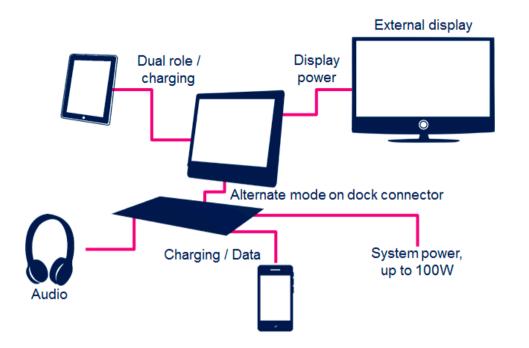
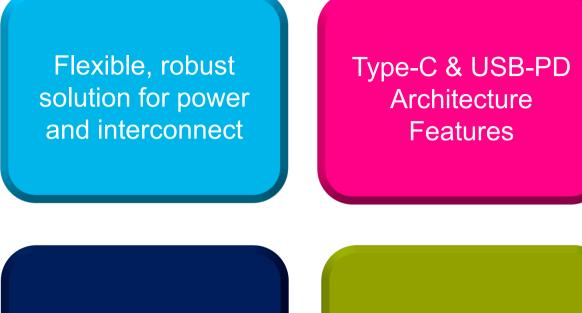
USB Power Delivery and Type-C[™]







ST products for Type-C & USB-PD

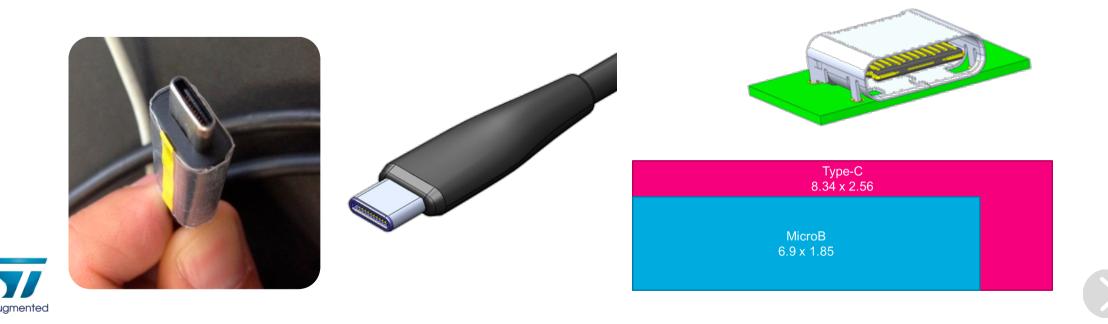
Type-C & USB-PD Solution



USB Type-C Overview

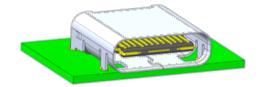
USB Power Delivery specification introduces USB Type-C receptacle, plug and cable; they provide a smaller, thinner and more robust alternative to existing USB interconnect. Main features are:

- Enable new and exciting host and device form-factors where size, industrial design and style are important parameters
- Work seamlessly with existing USB host and device silicon solutions
- Enhance ease of use for connecting USB devices with a focus on minimizing user confusion for plug and cable orientation



USB Type-C with Power Delivery





New USB Type-C receptacle, plug and cable; a smaller, thinner and more robust alternative to existing USB interconnect

Enables new and exciting host and device form-factors where size, industrial design and style are important parameters

Work seamlessly with existing USB host and device silicon solutions

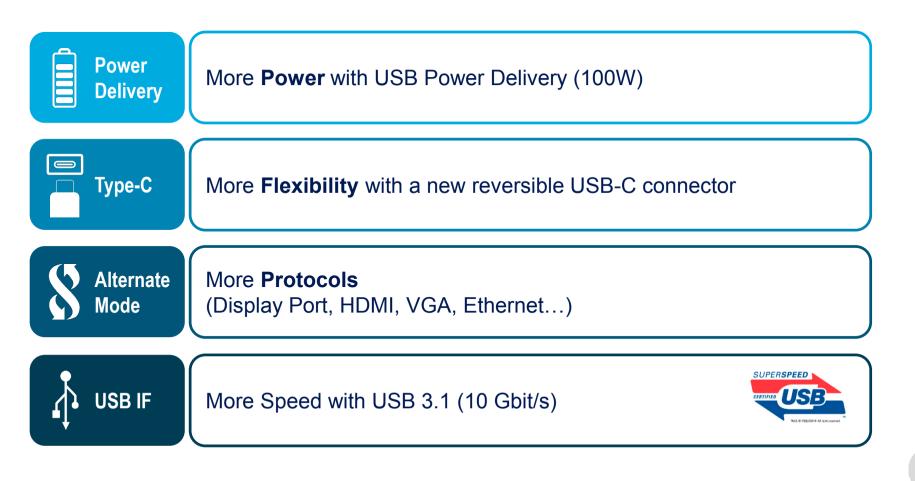
Enhances ease of use for connecting USB devices Focus on minimizing user confusion for plug and cable orientation





The Re-Evolution of USB

USB has evolved from a data interface capable of supplying limited power to a primary provider of *power* with a data interface





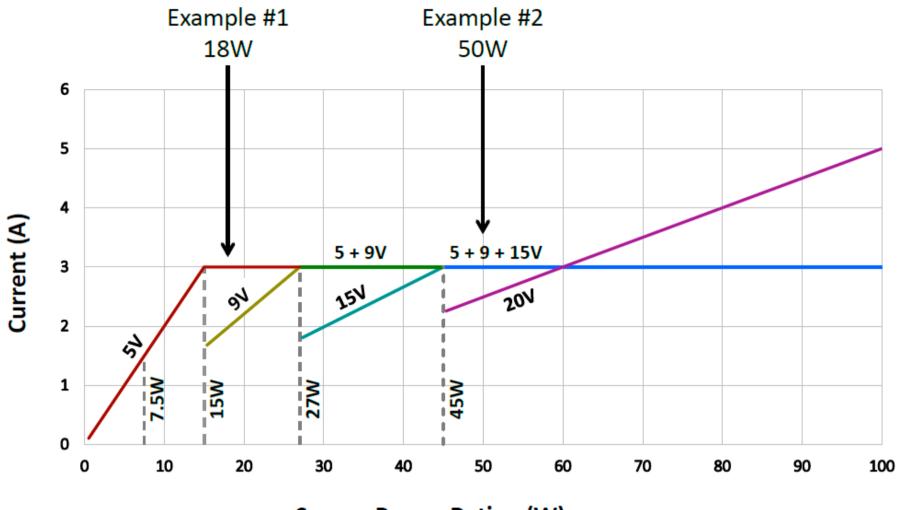


USB Type-C: More Power Options

Mode of Operation	Nominal Voltage	Maximum Current	Notes
USB 2.0	5 V	500 mA	Default current, based on definitions in the base
USB 3.1	5 V	900 mA	specifications
USB BC 1.2	5 V	Up to 1.5 A	Legacy charging
USB Type-C @ 1.5 A	5 V	1.5 A	Supports high power devices
USB Type-C @ 3.0 A	5 V	3 A	Supports higher power devices
USB PD	Configurable up to 20 V	Configurable up to 5 A	Directional control and power level management



USB Type-C Power Rules

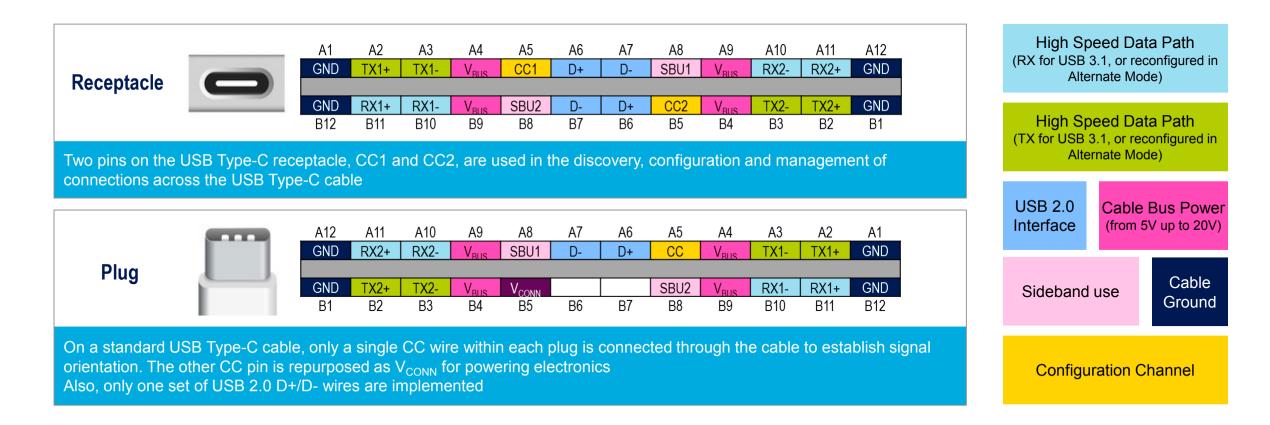




Source Power Rating (W)

USB Type-C[™] Pin Outs Functions

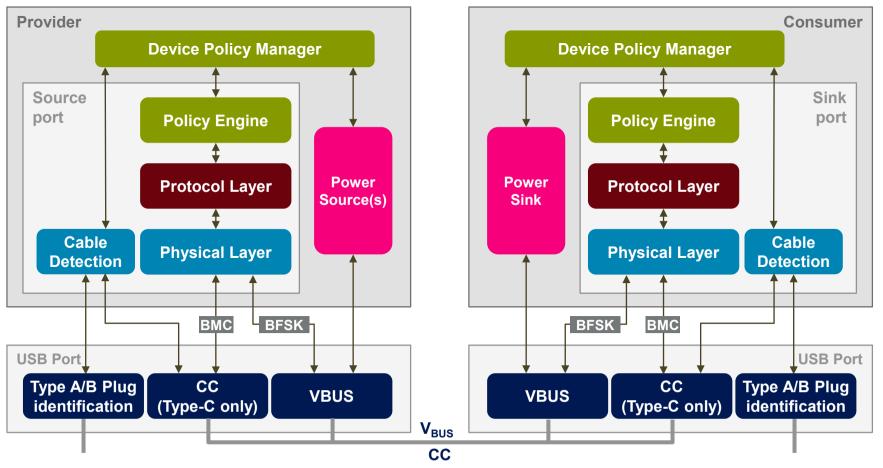
Enhance ease of use





Architecture

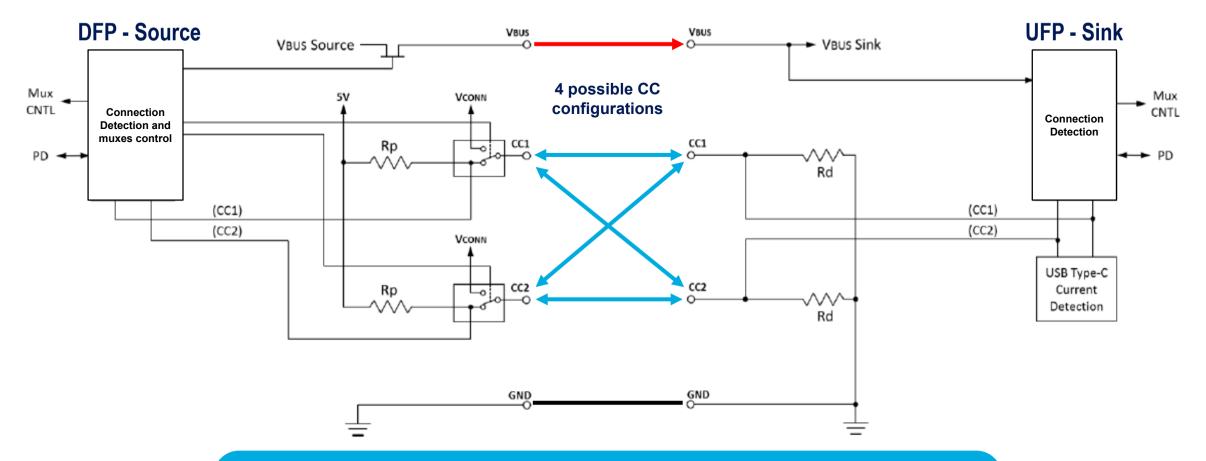
Architecture and key words





Communication across the channel uses Biphase Mark Coding (BMC) over CC in Type C connector

USB Type-C CC Connections



- Detect attach/detach of USB ports, e.g. a DFP to a UFP
- **Resolve cable orientation and twist connections** to establish USB data bus routing
- Establish DFP and UFP roles between two attached ports
- Discover and configure VBUS
- USB Power Delivery Communication



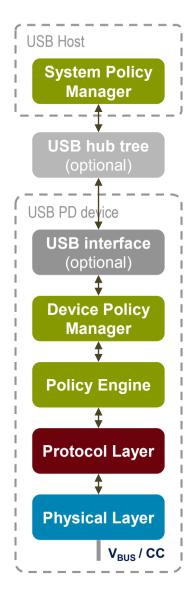
USB PD Stack & Policy

Policies

System Policy Manager (system wide) is optional. It monitors and controls System Policy between various Providers and Consumers connected via USB.

Device Policy Manager (one per Provider or Consumer) provides mechanisms to monitor and control the USB-PD within a particular Provider or Consumer. It enables local policies to be enforced across the system by communication with the System Policy Manager.

Policy Engine (one per Source or Sink Port) interacts directly with the Device Policy Manager in order to determine the present local policy to be enforced.



Protocol Layer

The Protocol Layer forms the messages used to communicate information between a pair of ports. It receives inputs from the Policy Engine indicating which messages to send and indicates the responses back to the Policy Engine

Physical Layer

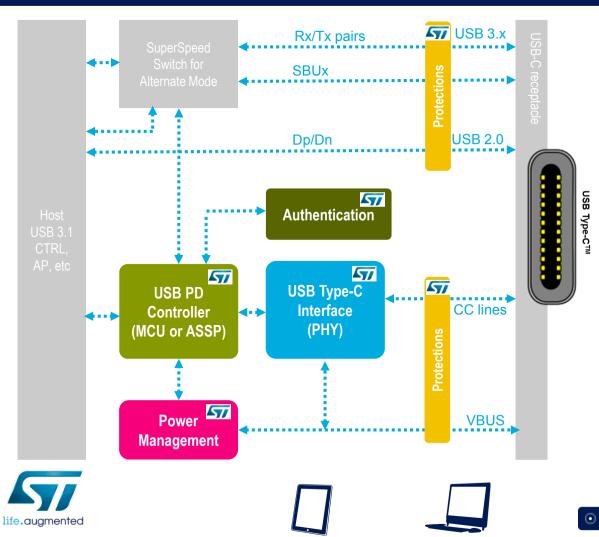
It is responsible for sending and receiving messages across either the V_{BUS} or CC wire. It consists of a transceiver that superimposes a signal (BFSK on V_{BUS} or BMC on CC) on the wire.

It is responsible for managing data on the wire and for collision avoidance and detects errors in the messages using a CRC



ST Products for USB Type-C PD

ST Chipset: A flexible offer in the USB Type-C PD ecosystem



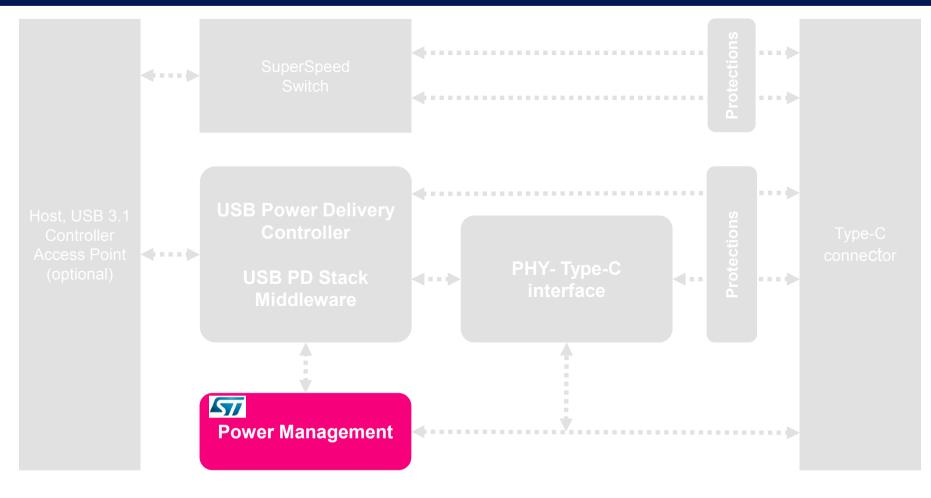
Scalable offer for USB-PD controller and USB Type-C interface: from STM32 general purpose MCU to hard-coded solution to fit different use cases and power ratings

Large product portfolio for protection and filtering covering all the application needs

Highly secure solution using STSAFE secure element family for strong authentication needs

Power Management

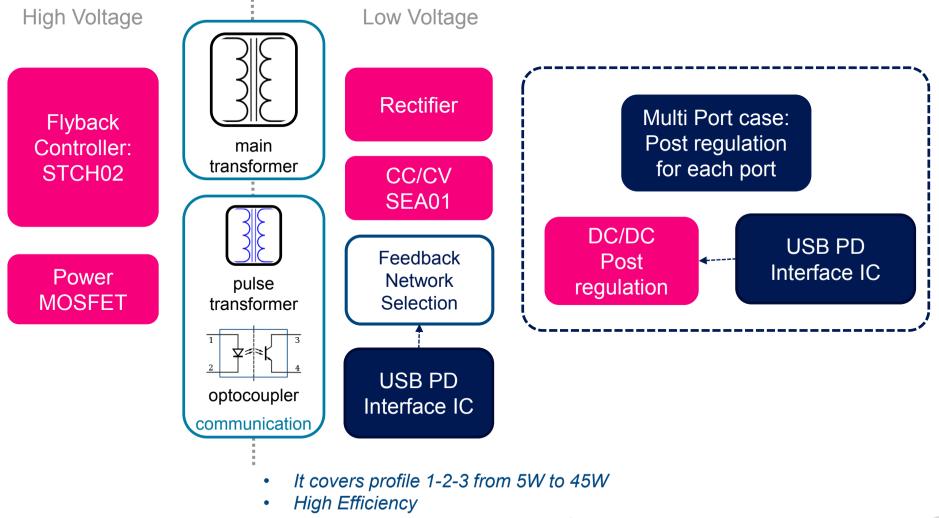
A complete offer to "lean in" USB PD Ecosystem







Profile 1-2-3 Power source building blocks

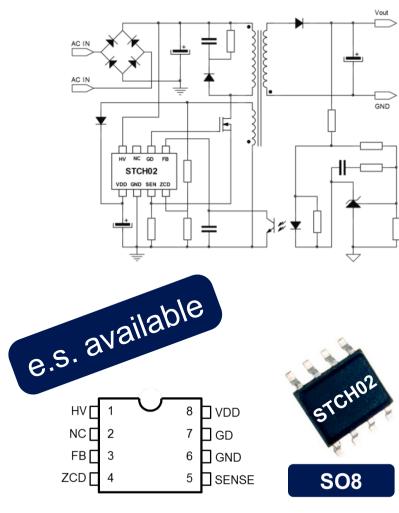


• Low EMI design: intelligent Jitter for EMI suppression



STCH02

Primary side controller: Adapters up to 45W

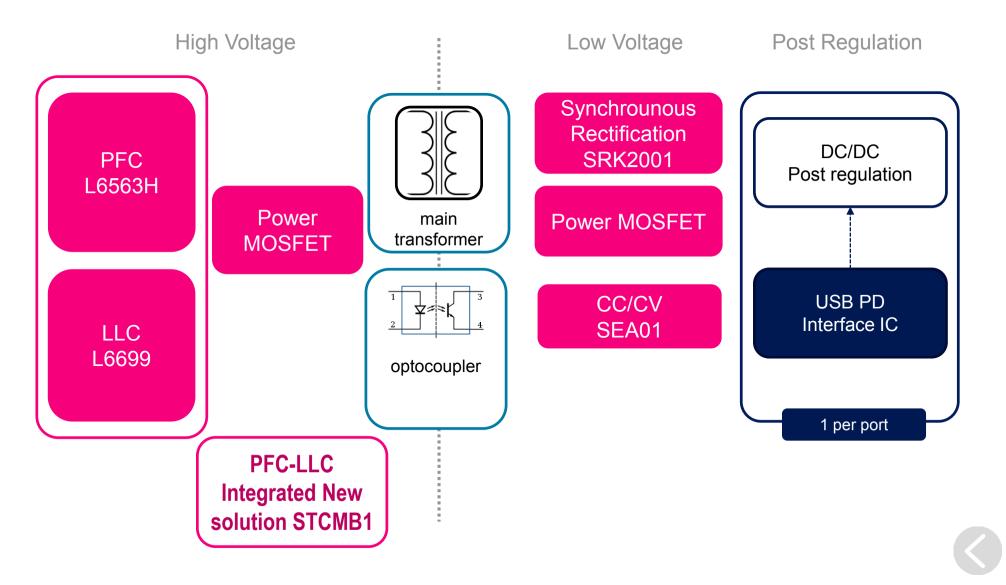


Features

- Proprietary Constant current output regulation (CC) with no opto-coupler
- 700V embedded HV start-up circuit
- Quasi-resonant (QR) Zero Voltage Switching (ZVS) operation
- Valley skipping at medium-light load and advanced burst mode operation at no-load for under 10mW consumption
- Accurate adjustable output OVP
- Low part count. BOM reduction thanks to an extensive features integration
- Exceeding 5 stars: No-Load power < 10mW
 - HV start-up zero power consumption
 - Advanced burst-mode operation
- Flexibility: suitable for adapters from 5W to 40W
- High Efficiency
- Low EMI design: intelligent jitter for EMI suppression



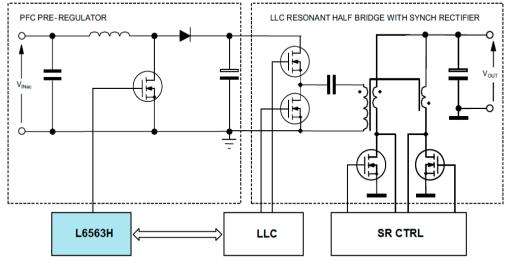
Profile 4, 5 Power source building blocks





L6563H

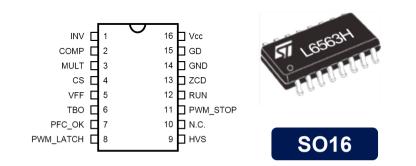
Transition mode PFC controller



Datasheet : available on www.st.com

Features

- 700V High Voltage Start-up circuit
- Fast bidirectional input voltage feed-forward
- Adjustable OVP
- AC Brownout Detection
- Tracking boost function
- Inductor saturation protection
- Proprietary THD optimizer circuit
- Interface for cascaded converters
- -600mA/+800mA gate driver



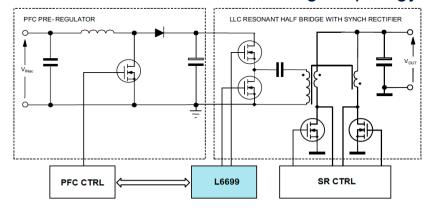
- Low steady state ripple and current distortion with limited under- or overshoot of the pre-regulator's output thanks to new input voltage feed-forward implementation
- Reduced THD of the current
- High reliability thanks to a full set of protections
- HV start-up significantly reduces consumption compared to standard discrete circuit solutions
- Facilitated cooperation with cascaded DC-DC converter thanks to several power management & housekeeping functions



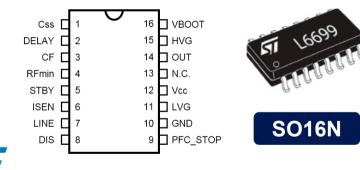
L6699

High power adapters 90W to 250W

Series-resonant half-bridge topology



Datasheet : available on www.st.com

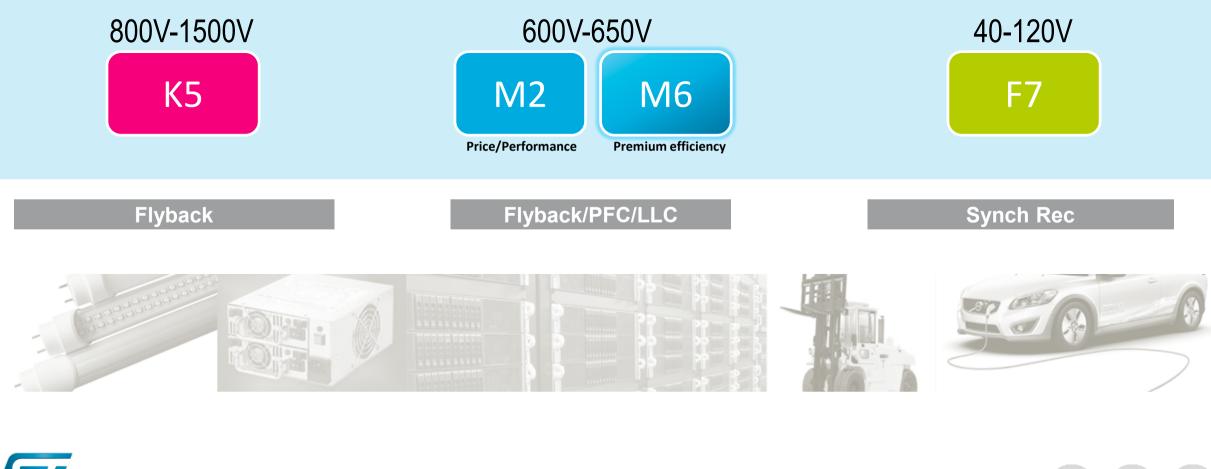


Features

- Self adjusting adaptive dead time
- Anti-capacitive mode protection
- Two-level OCP: Frequency shift and Immediate shutdown
- Safe-start procedure
- Burst-mode operation at light load
- Brown-out protection
- · Interface with PFC controller
- High efficiency:
 - Reduced internal consumption (Iq=1mA)
 - Adaptive dead time allows design optimization to achieve ZVS with lower magnetizing current
- Improved reliability and lifetime thanks to anti-capacitive protection and smooth start-up circuit
- Reduced audible noise when entering burst-mode operation thanks to smooth restart feature



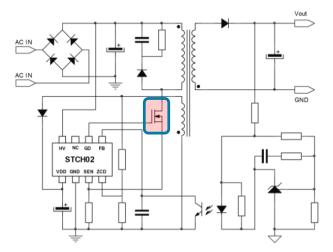
USB-PD Power MOSFET product families



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VHV Power MOSFETs

Flyback Architecture



Outstanding Form Factor



Features

- Unmatched R_{DS(on)} at very high BVDSS 800-950V-1050V
- Ultra-Low Q_G and high switching speed
- Extremely low thermal resistance
- High quality & reliability

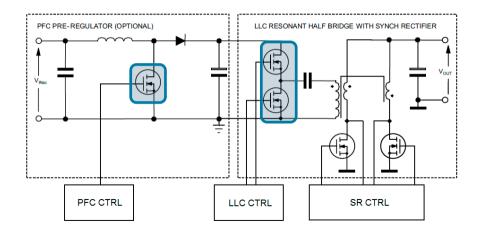
Benefits

- Lower on-state conduction losses
- Best switching losses
- High efficiency with lower design complexity
- Ultra small Form factor

Product range example

Part Number	B _{VDss}	R _{DS(on)}	I _D
STB13N80K5	800V	0.45Ω	12A
STD8N80K5	800V	0.95Ω	6A
STD9N80K5	800V	0.90Ω	7A





Product range example

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PFC	V _{DSS}	R _{DS(on)}	l _D	
STF24N60M2	600V	0.190Ω	18A	PFC
STF25N60M2-EP	600V	0.188Ω	18A	Performance
STF20N60M2-EP	600V	0.278Ω	13A	
LLC	V _{DSS}	R _{DS(on)}	I _D	
STF9N60M2	600V	0.750Ω	5.5A	
STF15N60M2-EP	600V	0.378Ω	11A	LLC
STFI11N60M2-EP (e.s.available)	600V	0.595Ω	8.0A	Performance
LLC	V _{DSS}	R _{DS(on)}	ا _D	
STF9N60M6	600V	0.750Ω	ES April '16	LLC
STF10N60M6	600V	0.600Ω	ES April '16	Premium
STF13N60M6	600V	0.380Ω	ES April '16	

Power MOSFET PFC & LLC architecture

Features

- Up to 30% lower Q_G vs main competition (equivalent die size)
- 400 700V BV_{DSS} rated
- Back-to-Back G-S Zener protected

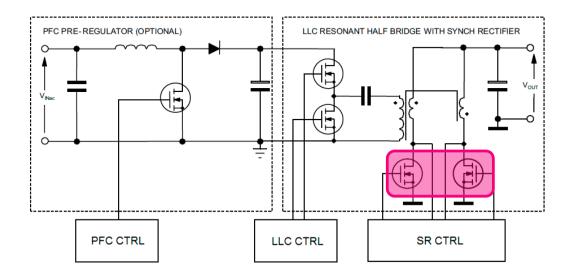
Benefits

- Reduced switching losses
- Enhanced immunity vs ESD & Vgs spikes
- Technologies dedicated to specific topology

Product range example



Power MOSFETs Synchronous rectification



Part Number	Voltage	Ron	Current
STL260N4LF7	40V	<1.1mΩ	5.5A
STL200N45LF7	40V	<1.8mΩ	11A



Benefits

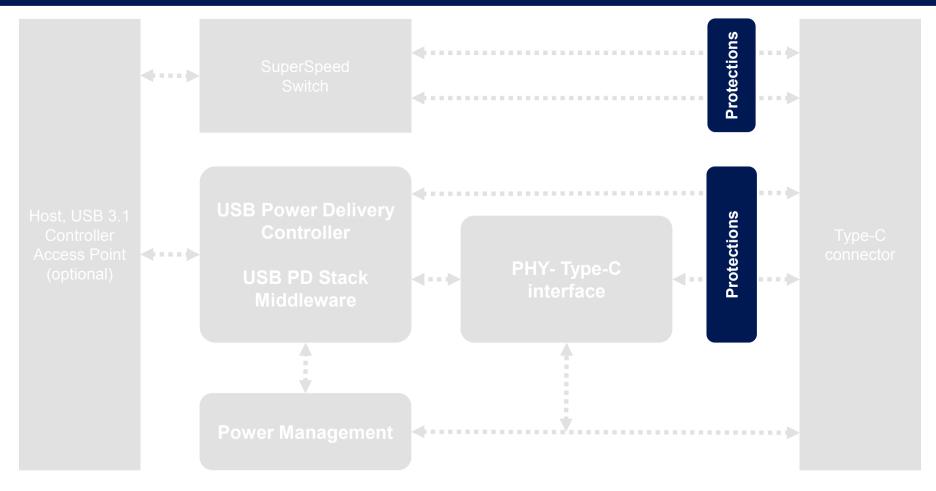
- Efficiency improvement due low conduction losses and to static and dynamic diode ones, minimized switching noise and Vds spike at turn OFF
- Easy driving features





Protections

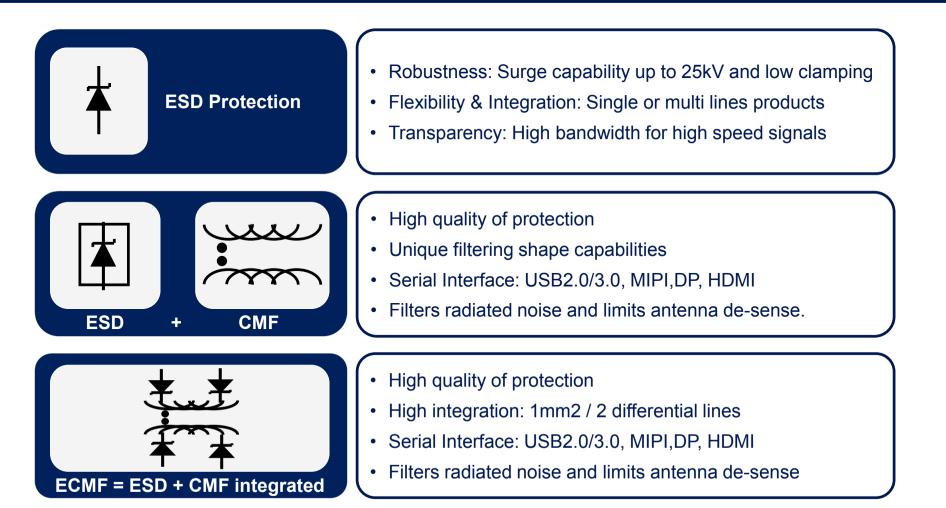
A complete offer to "lean in" USB PD Ecosystem





Protections ESD/CMF/ECMF

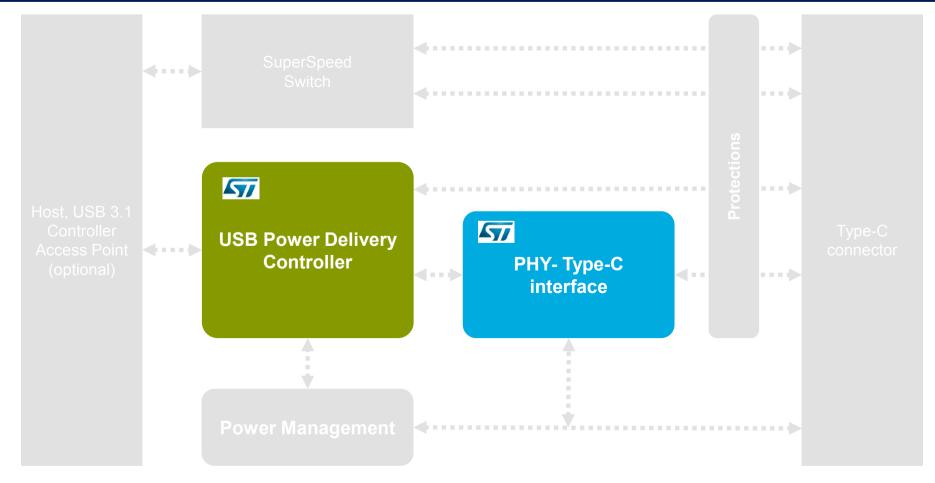
High flexibility for the Designers needs to find best compatibilities



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Type-C and USB PD Controller

A complete offer to "lean in" USB PD Ecosystem

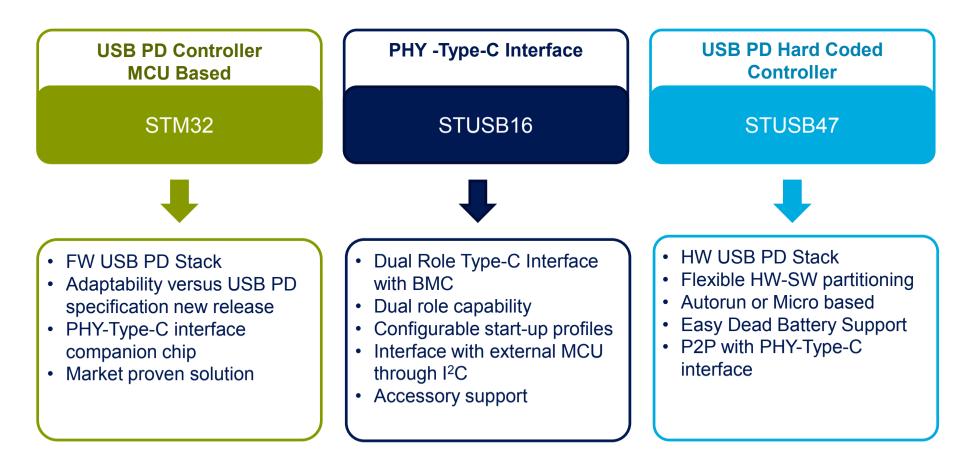






Controller & Interface

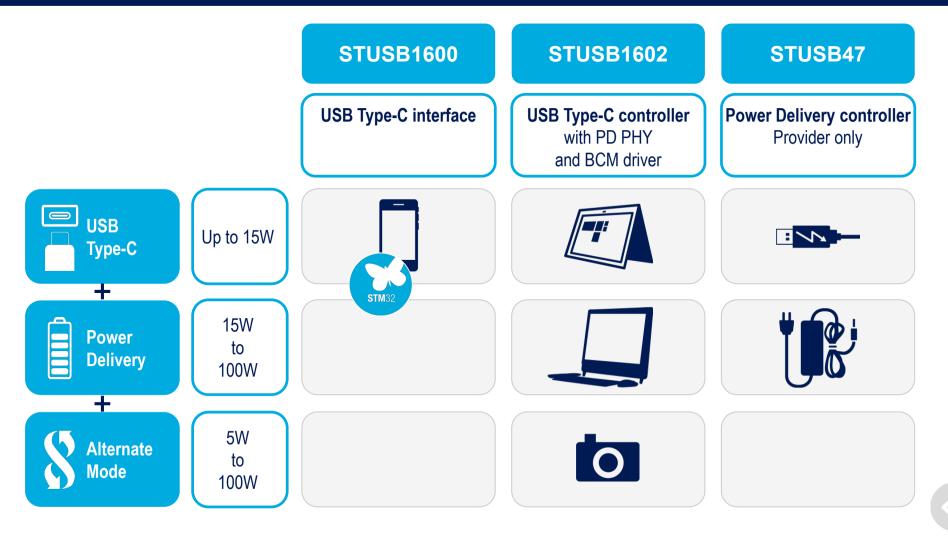
Offering flexible and scalable solutions for designers





USB Type-C[™] and USB PD Controller

STUSB Family: from USB Type-C Interface to 100% HW Power Delivery Controllers

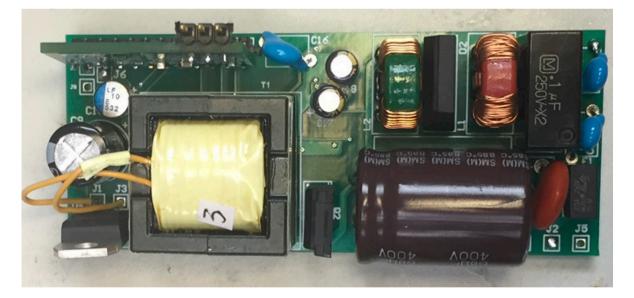


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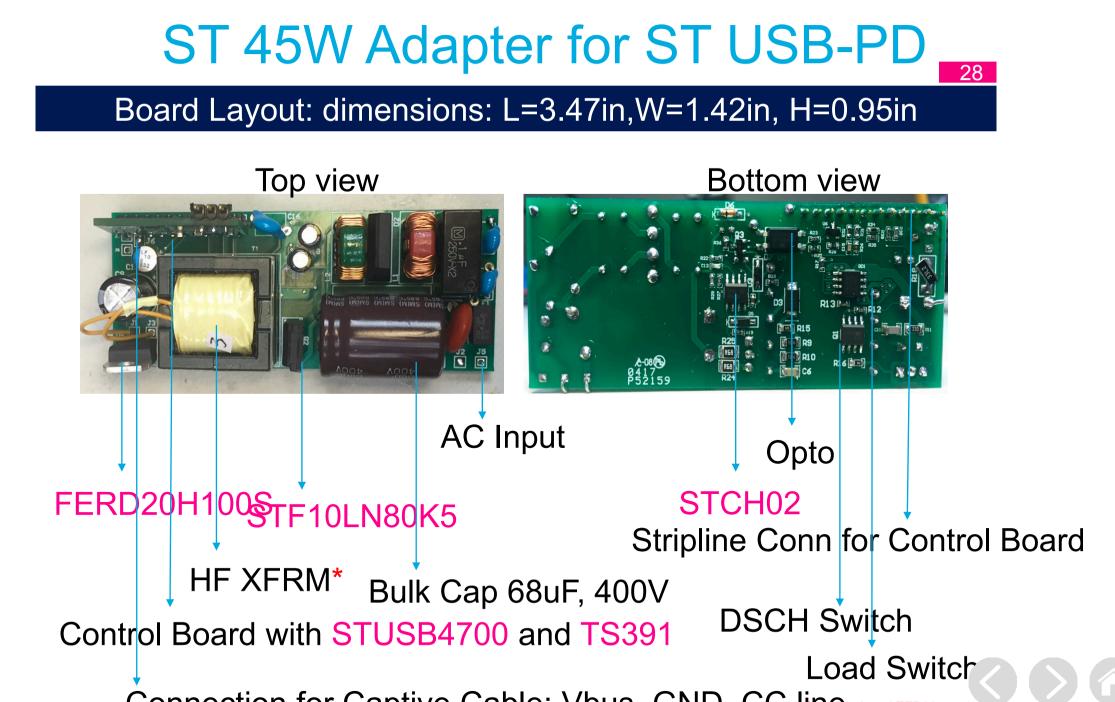
USB-PD Provider Solution

- AC/DC Multi-output 45W Converter
 - Based on STCH02 QR controller
 - Multiple Output voltages (5V, 9V, 12V)

- STUSB4700 hard coded controller
 - to interface with USB-C connector
 - to handle the USB Power Delivery protocol
 - No software, better reliability







Connection for Captive Cable: Vbus, GND, Coldmeeplaced EFD20

USB PD 2.0 Vs. USB PD 3.0 29

USB 3.0 ensures full compatibility with respect to USB 2.0 and requires some additional optional and mandatory features

New mandatory features in USB PD 3.0	Enables	
Battery status data message (Required for systems with batteries)	Reporting of the battery state of charge	
Battery capabilities extended message (Required for systems with batteries)	Reporting of the battery design capacity and last full charge capacity	
Tighter control on communication to the cable (Required)	Only the V_{CONN} source to communicate to the cable plug e-marker	
Source-coordinated collision avoidance (Required)	A more robust mechanism to avoid collisions caused when both source and sink want to send messages beyond the typical power negotiation. USB PD 2.0 has a collision-avoidance mechanism, but with the many new optional messages that may be sent in USB PD 3.0 a more robust method was needed	
Not supported control message (Required)	A way to inform a port partner that a particular message is not supported. This is meant to help in the future as USB PD 3.0 expands to include more options and features.	

- The voltages required by the new "Power Rules" are 5V, 9V, 15V, and 20V.
- USB PD 3.0 new features are not necessary in all application
- A simple Source device may not need USB PD 3.0 new features



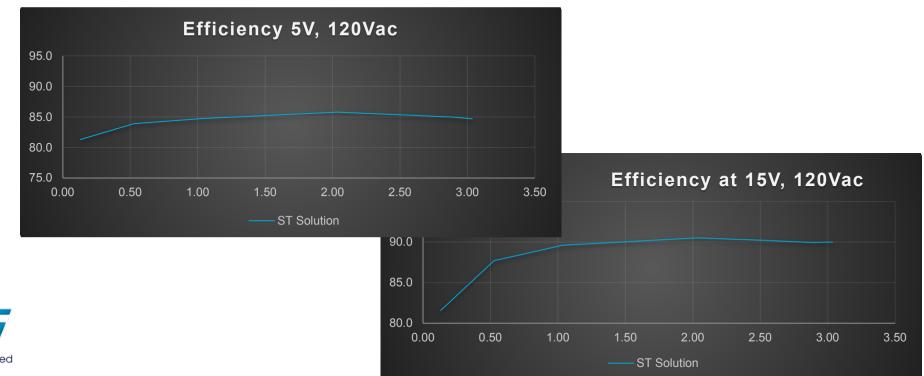


For others optional features visit <u>http://www.usb.org/developers/powerdelivery/</u>

Efficiency: 120VAC Input Voltage

- Power measured at output capacitor
- Tested at 5V and 15V; Load current swept from 0.1A to 3A
- ST Nucleo USB-PD board connected as a host
- Input Power Measured with Yokogawa WT200
- Output current and Voltage measured with Fluke True RMS Digital Multimeters
- Efficiency measured at room temperature after 20 min operation at full power
- No cable drop compensation

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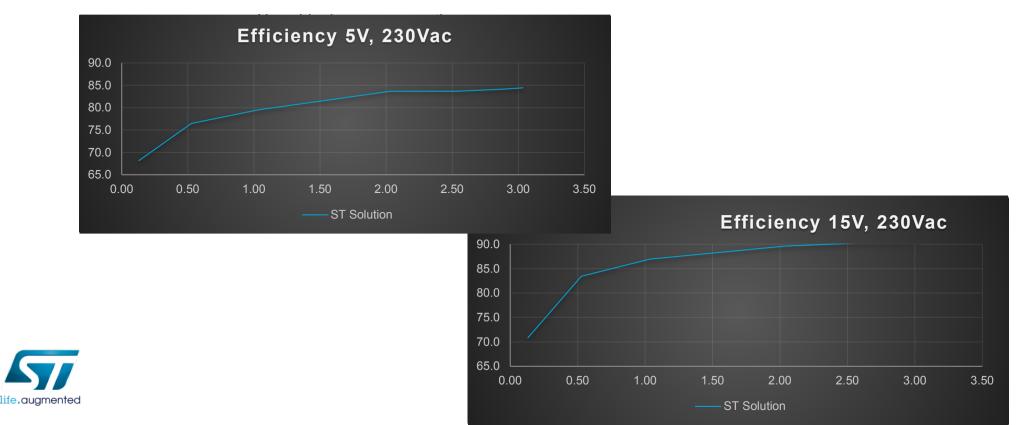
Efficiency

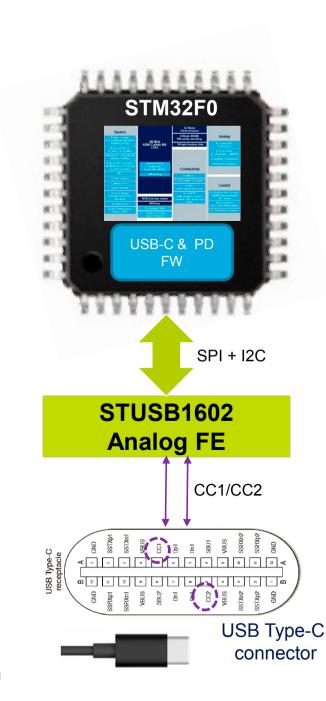
Efficiency

31

Efficiency: 230VAC Input Voltage

- Power measured at output capacitor
- Tested at 5V and 15V; Load current swept from 0.1A to 3A
- ST Nucleo USB-PD board connected as a host
- Input Power Measured with Yokogawa WT200
- Output current and Voltage measured with Fluke True RMS Digital Multimeters
- Efficiency measaured at room temperature after 20 min operation at full power





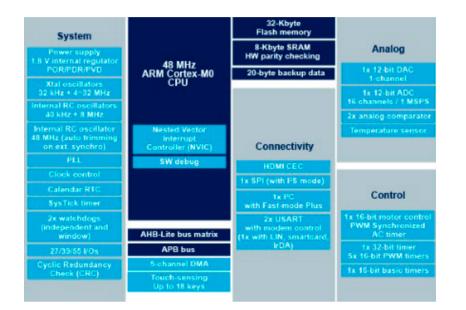
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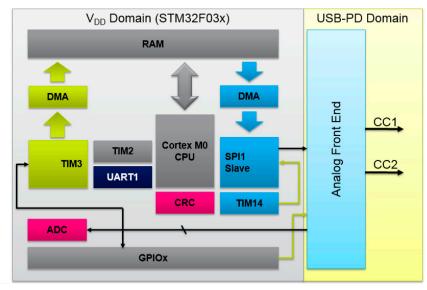
USB-PD Dual Role Solution MCU + Analog Front-End overview

- STM32 Embedded Software Solution + STUSB1602 AFE
 - Provide Flexibility
 - Possibility to customize the software
- Hardware : Entry level Cortex-M0 based STM32F0 microcontroller series with simple discrete Analog Front End PHY
- Embedded Software : USB-C & PD Middleware

Best device for 2 ports management : STM32F051 in 48 pin package Best device for 1 port management : STM32F051/31 in 20/32 pin package







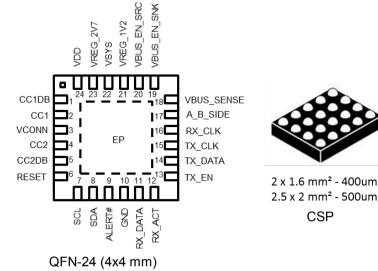
MCU Overview STM32F0 HW resources

- Transmission uses : TIM14, SPI1, DMA, GPIO
- Reception uses : TIM3, DMA, 1 comparator
- TIM2 is used to time-schedule tasks
- Embedded ADC to detect device on the CC bus and perform power measurements
- CRC to evaluate message's CRC
- Standard GP I/Os to control Vconn, Load switch, Vbus discharge switch, Vout selection (primary feedback...

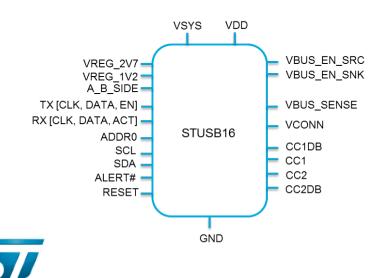
Project	Flash Memory	RAM Memory
Provider only	25.5 kB	4.4 kB
Provider only (RTOS)	29.0 kB	7.3 kB
Provider/Consumer DRP (RTOS)	30.2 kB	7.3 kB



USB-PD Interface STUSB16xx



Pin to pin compatible with STUSBxx family



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Features

- Dual Role Type-C Interface with BMC
- Dual role capability
- Configurable start-up profiles
- 600mA VCONN
- 120uA Idle current measured
- Interface with external MCU through I²C+Interrupt
- Integrated Voltage monitoring
- Integrated V_{BUS} discharge path
- Accessory support
- Dual Power supply:
 - V_{SYS} = 3.3V,
 - V_{DD} [4.6V; 22V] (from V_{BUS})

Benefits

- Low Pin count
- Integrated BMC transceiver
- Simple, Robust
- Configurable, Flexible
- Optimized for Portable applications
- P2P with STUSB4x

X-NUCLEO-USBPDM1

- USB-C Power Delivery expansion board with two USB Type-C connectors for two port management
- Main features:
 - Two Dual Role Port
 - Dedicated Power Connector to interface with external Power Supply board providing different profiles (up to 20V and 5A) and $V_{\rm CONN}$
 - On-board Power management able to provide internal needed voltages from V_{BUS}
 - Six debug LEDs
 - USB 2.0 interface capability available on one port
 - Compatible with STM32 Nucleo boards
 - Equipped with ST morpho connectors





X-NUCLEO-USBPDM1

