

# Product Errata LTC3884 September 2016

## LTC3884 ERRATA

The errata below describes conditions that cause an LTC®3884 device to operate differently than expected or described in the data sheet.

# ERRATA #1: WRITING VOUT\_MAX CHANGES PWM MODE

If the user writes the VOUT\_MAX command while device is active, the device may change from discontinuous mode to continuous mode, or from continuous mode to discontinuous mode.

### **Conditions:**

The following conditions will expose this problem:

- Place either channel into discontinuous conduction mode by clearing bit #0 of the MFR\_PWM\_MODE (0xD4).
- 2) Turn the channel on either through the RUN pin or the OPERATION command depending upon the value in the ON\_OFF\_CONFIG PMBus command.
- 3) Perform a PMBus read word of the VOUT\_MAX (0x24) command.
- 4) Write PMBus write word of the VOUT\_MAX (0x24) using the value obtained from step 3.

#### Impact:

The output switching mode will change from user requested conduction mode.

#### **Root Cause:**

Writing the VOUT\_MAX command refreshes all of the trim values to the analog registers. One of these registers not only contains analog trim information, but also the instruction to the channel PWM regarding continuous conduction mode. This operation does not take into account the user instruction in MFR\_PWM\_MODE regarding continuous conduction modes.

**Δ**, LT, LTC, LTM, Linear Technology and the Linear logo are registered trademarks of Linear Technology Corporation. All other trademarks are the property of their respective owners.

#### Workarounds:

The user should avoid writing the VOUT\_MAX command while either channel of the part is active. If the user must write the VOUT\_MAX command value, rewrite the value of the MFR\_PWM\_MODE command.

## ERRATA #2: STATUS\_WORD VS. STATUS\_BYTE

There is a small probability that bits in the STATUS\_BYTE may be asserted when the STATUS\_WORD LSB reports all zero.

#### **Conditions:**

The following conditions, when present simultaneously, may expose this problem:

1) When polling STATUS\_WORD, if a fault occurs at the right time, the read value can have a bit set in the lower byte with no corresponding bits set in the upper byte.

#### Impact:

A minor inconsistency can occur when customer firmware is polling the STATUS\_WORD register of the part.

#### **Root Cause:**

The two bytes making up STATUS\_WORD are transferred separately to the PMBus controller of the LTC3884. If STATUS\_WORD LSB and MSB values are being transferred to the user when a status event occurs, an incoherent read can be reported to the user.

#### Workarounds:

Several workarounds are possible, depending on the system configuration and requirements. Contact LTC Factory Applications for additional assistance.

- 1) Poll STATUS\_BYTE instead of STATUS\_WORD. STATUS\_BYTE indicates the part status sufficiently.
- 2) Poll STATUS\_WORD twice if a discrepancy is detected. STATUS\_WORD will poll correctly immediately after the first incorrect reading.

Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.

er3884f

1