

4V Drive Pch MOSFET

RP1E075RP

Structure

Silicon P-channel MOSFET

Features

- 1) Low On-resistance.
- 2) High power package.
- 3) 4V drive.

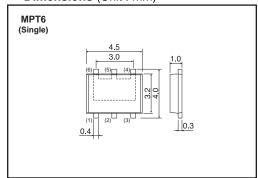
Application

Switching

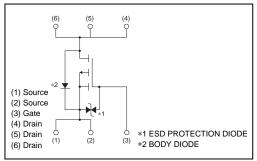
Packaging specifications

	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	1000
RP1E075RP		0

• Dimensions (Unit : mm)



• Inner circuit



● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	-30	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	Continuous	I _D	±7.5	Α
	Pulsed	I _{DP} *1	±30	Α
Source current	Continuous	Is	-1.6	Α
(Body Diode)	Pulsed	I _{SP} *1	-30	Α
Power dissipation		P _D *2	2.0	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10µs, Duty cycle≤1%

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	62.5	°C / W

^{*}Mounted on a ceramic board.

^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	±10	μA	$V_{GS}=\pm20V$, $V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	-30	-	-	V	I _D =-1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	-1	μA	V_{DS} =-30V, V_{GS} =0V
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	V_{DS} =-10V, I_{D} =-1mA
	*	1	15	21		I _D =-7.5A, V _{GS} =-10V
Static drain-source on-state resistance	R _{DS (on)}	1	22	31	mΩ	I _D =-4A, V _{GS} =-4.5V
resistance		-	25	35		I _D =-4A, V _{GS} =-4.0V
Forward transfer admittance	IY _{fs} I*	9	-	-	S	I _D =-7.5A, V _{DS} =-10V
Input capacitance	C _{iss}	1	1900	-	pF	V _{DS} =-10V
Output capacitance	C _{oss}	-	250	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	250	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	14	-	ns	I _D =-4A, V _{DD} ≒-15V
Rise time	t _r *	1	25	-	ns	V _{GS} =-10V
Turn-off delay time	t _{d(off)} *	1	100	-	ns	$R_L=3.8\Omega$
Fall time	t _f *	1	70	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	1	21	-	nC	I _D =-7.5A, V _D ;≒-15Ω
Gate-source charge	Q _{gs} *	1	5	-	nC	V_{GS} =-5V R_L =2.0 Ω
Gate-drain charge	Q _{gd} *	-	7	-	nC	$R_G=10\Omega$

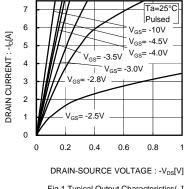
^{*}Pulsed

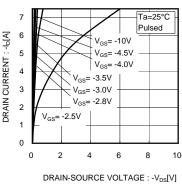
●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	-1.2	V	I _s =-7.5A, V _{GS} =0V

^{*}Pulsed

Electrical characteristic curves





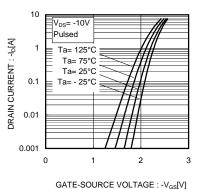


Fig.1 Typical Output Characteristics(I)

Fig.2 Typical Output Characteristics(II)

Fig.3 Typical Transfer Characteristics

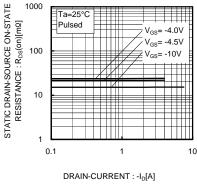


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

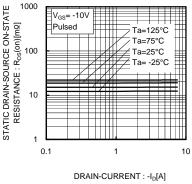


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

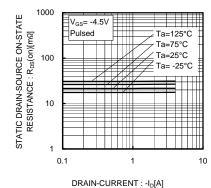


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(III)

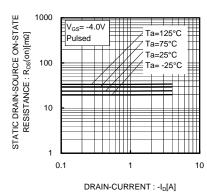
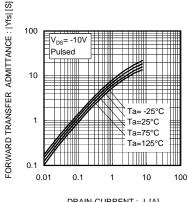
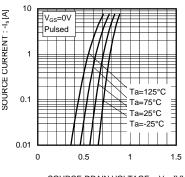


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(IV)



DRAIN-CURRENT : -I_D[A] Fig.8 Forward Transfer Admittance vs. Drain Current



SOURCE-DRAIN VOLTAGE: -V_{SD} [V] Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage

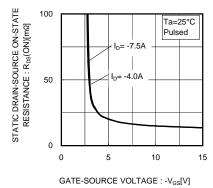


Fig.10 Static Drain-Source On-State Resistance vs. Gate Source

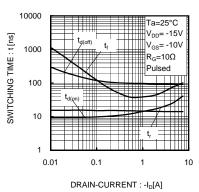
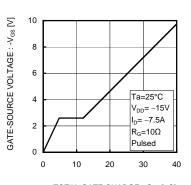
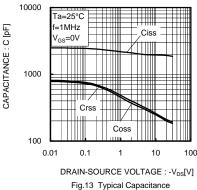


Fig.11 Switching Characteristics



TOTAL GATE CHARGE : Qg [nC] Fig.12 Dynamic Input Characteristics



vs. Drain-Source Voltage

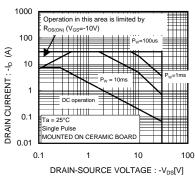


Fig.14 Maximum Safe Operating Aera

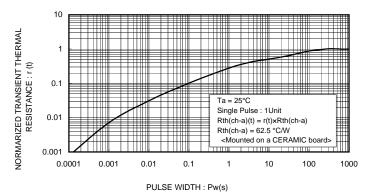


Fig.15 Normalized Transient Thermal Resistance vs. Pulse Width

Measurement circuits

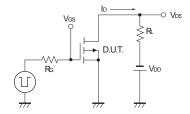


Fig.1-1 Switching Time Measurement Circuit

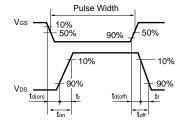


Fig.1-2 Switching Waveforms

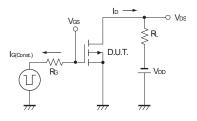


Fig.2-1 Gate Charge Measurement Circuit

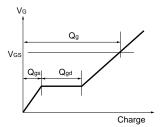


Fig.2-2 Gate Charge Waveform

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