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February 2015

# MMSD3070 Small Signal Diode



SOD123
COLOR BAND DENOTES CATHODE TOP MARKING: 33

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method	
MMSD3070	33	SOD-123 2L	Tape and Reel	

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter		Value	Unit
$V_{RRM}$	Maximum Repetitive Reverse Voltage		200	V
I <sub>F(AV)</sub>	Average Rectified Forward Current		200	mA
1=014	Non-Repetitive Peak Forward Surge Current	Pulse Width = 1.0 second	1.0	- А
		Pulse Width = 1.0 microsecond	2.0	
T <sub>STG</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>J</sub>	Operating Junction Temperature		150	°C

### **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P <sub>D</sub>	Power Dissipation	400	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	312	°C/W

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 100 μA	200		V
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 100 mA		1.0	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 175 V		100	nA
		V <sub>R</sub> = 175 V, T <sub>A</sub> = 150°C		100	μΑ
C <sub>T</sub>	Total Capacitance	V <sub>R</sub> = 0, f = 1.0 MHz		5.0	pF
t <sub>rr</sub>	Reverse Recovery Time	$I_F = I_R = 30 \text{ mA}, I_{RR} = 1.0 \text{ mA},$ $R_L = 100 \Omega$		50	ns

## **Typical Performance Characteristics**

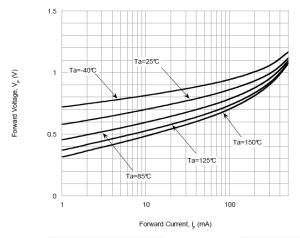


Figure 1. Forward Voltage vs. Forward Current

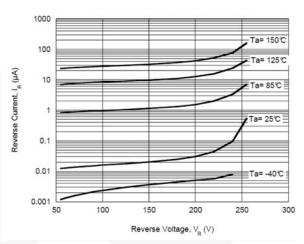


Figure 2. Reverse Current vs. Reverse Voltage

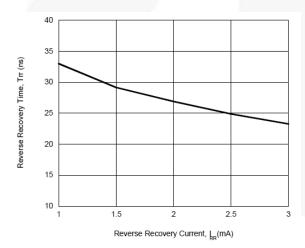


Figure 3. Reverse Recovery Time vs. Reverse Recovery Current

# **Physical Dimensions** Α 1.80 1.40 В **0.88 MIN** 2.85 3.27 2.55 1.02 MIN 0.70 0.50 0.10 M BS AS LAND PATTERN **RECOMMENDATION TOP VIEW** 1.28 SEATING PLANE 0.88 1.18 0.18 0.88 0.08 3.90 3.60 FRONT VIEW SIDE VIEW NOTES: UNLESS OTHERWISE SPECIFIED A) PACKAGE REFERENCE: JEDEC, DO-215 ISSUE D, VARIATION AD. **GAGE PLANE** B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. 0-8° E) DRAWING FILE NAME: MA02AREV4 0.40 0.12 0.23 0.00 DETAIL "A" SCALE 2:1

Figure 4. 2-LEAD, SOD123, JEDEC DO-219





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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