



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
	40mΩ @ V _{GS} = 10V	5.5A
60V	55mΩ @ V _{GS} = 4.5V	4.7A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

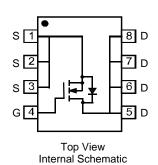
Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (approximate)





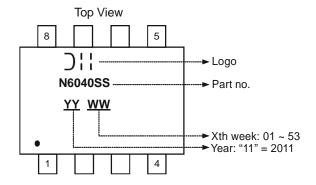
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN6040SSS-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and 1000ppm antimony compounds.
 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information





Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Compart (Note C) // 40)/	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I _D	5.5 4.4	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = 25$ °C $T_A = 70$ °C	I _D	7.0 5.5	А
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	2.5	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	30	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AR}	14.2	Α
Repetitive Avalanche Energy (Note 7) L = 0.1mH	E _{AR}	10	mJ		

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Total Power Dissipation (Note 5)	$T_A = 25$ °C	6	1.5	W	
Total Power Dissipation (Note 5)	$T_A = 70$ °C	P_{D}	1		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	_	80	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	48		
Total Bower Dissipation (Note 6)	$T_A = 25$ °C	Ć.	2.0	W	
Total Power Dissipation (Note 6)	$T_A = 70$ °C	P_D	1.3	VV	
Thermal Begintance, Junction to Ambient (Note 6)	Steady State	6	61		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	37	°C/W	
Thermal Resistance, Junction to Case		$R_{ heta JC}$	6.4		
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to 150	°C	

Electrical Characteristics T_A = 25°C unless otherwise specified

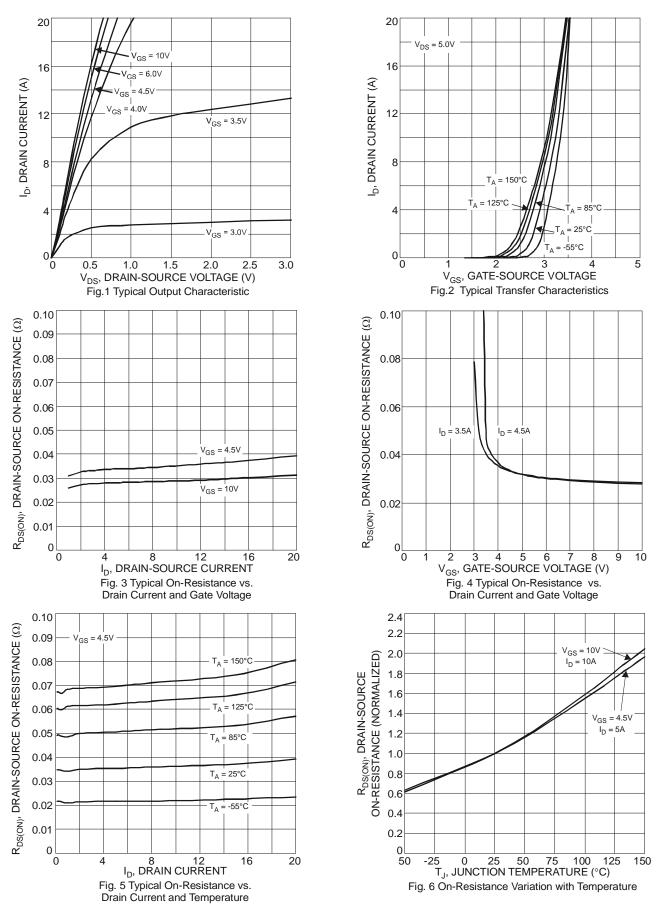
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_{D} = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	nA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(th)}$	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	30	40	mΩ	$V_{GS} = 10V, I_D = 4.5A$	
Static Dialif-Source Off-Resistance	R _{DS (ON)}	_	35	55		$V_{GS} = 4.5V, I_D = 3.5A$	
Forward Transfer Admittance	Y _{fs}	_	4.5	_	S	$V_{DS} = 10V, I_D = 4.3A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C_{iss}	_	1287	_		V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	57	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	44	_			
Gate Resistance	R_{G}	_	1.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	22.4	_		V _{DS} = 30V, I _D = 4.3A	
Total Gate Charge (V _{GS} = 4.5V)	Q_{g}	_	10.4	_	nC		
Gate-Source Charge	Q _{gs}	_	4.9	_	i iiC		
Gate-Drain Charge	Q_{gd}	_	3.0	_			
Turn-On Delay Time	t _{D(on)}	_	6.6	_			
Turn-On Rise Time	tr	_	8.1	_	nS	$V_{GS} = 10V, V_{DD} = 30V, R_G = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}	_	20.1	_	1115	$I_D = 4.3A$	
Turn-Off Fall Time	t _f	_	4.0	_			
Body Diode Reverse Recovery Time	t _{rr}	_	18	_	nS	I _S = 4.3A, dI/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{rr}	_	11.9	_	nC	$I_S = 4.3A$, $dI/dt = 100A/\mu s$	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = 25°C Notes:

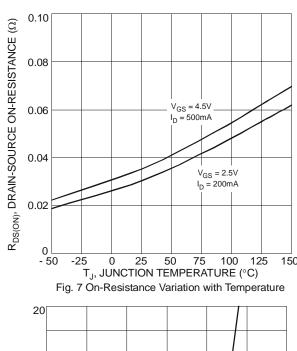
^{8.} Short duration pulse test used to minimize self-heating effect.

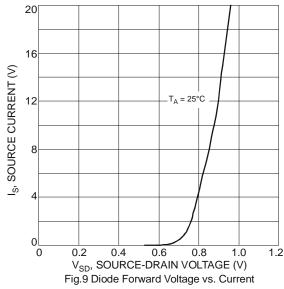
^{9.} Guaranteed by design. Not subject to product testing.

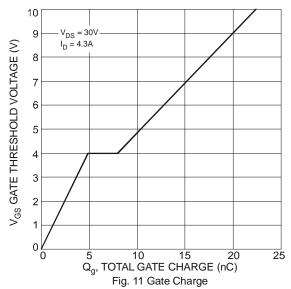












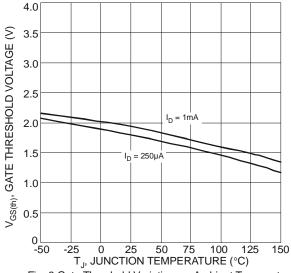
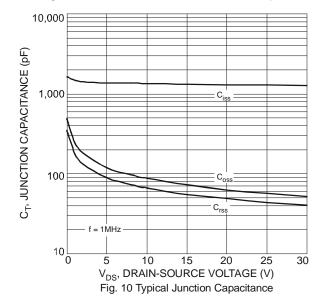
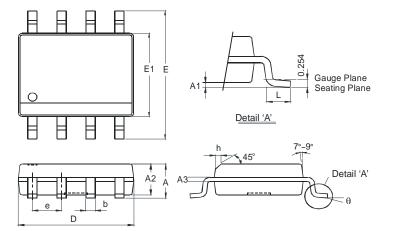


Fig. 8 Gate Threshold Variation vs. Ambient Temperature



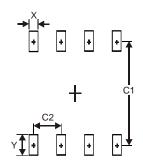


Package Outline Dimensions



SO-8				
Dim	Min	Max		
Α	1	1.75		
A 1	0.10	0.20		
A2	1.30	1.50		
А3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)		
Х	0.60		
Υ	1.55		
C1	5.4		
C2	1.27		



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