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June 2015

FPF2G120BF07ASP F2, 3ch Boost module PCM and NTC

General Description

The FPF2G120BF07ASP is the 3ch boost topology which is providing an optimized solution for the multi-string solar application. And the integrated high speed field stop IGBTs and SiC diodes are providing lower conduction and switching losses. And the pre-applied PCM requires no additional process of the thermal interface material printing. Furthermore, the screw clamp provides a fast and reliable mounting method.

* typical appearance

Package Code: F2

Electrical Features

- · High Efficiency
- · Low Conduction and Switching Losses
- · High Speed Field Stop IGBT
- · SiC SBD for Boost Diode
- · Built-in NTC for Temperature Monitoring

Mechanical Features

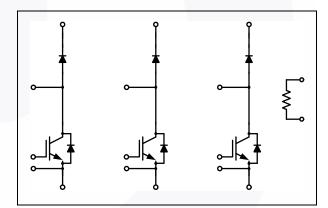
- · Compact Size : F2 Package
- · Soldering Pin
- Al₂O₃ Substrate with Low Thermal Resistance
- Pre-applied PCM (Phase Change Material)

Applications

· Solar Inverter

Related Materials

- AN-5077: Design Considerations for High Power Module (HPM)
- AN-4186: F1 and F2 Modules with Pre-applied Phase Change Material (PCM)



Internal Circuit Diagram

Package Marking and Ordering Information

Device	Device Marking	Package	PCM	Packing Type	Quantity / Tray
FPF2G120BF07AS	FPF2G120BF07AS	F2	Х	Tray	14
FPF2G120BF07ASP	FPF2G120BF07ASP	F2	0	Tray	14

Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Description Condition		Rating	Units	
Boost IGBT	•				
V _{CES}	Collector-Emitter Voltage		650	V	
V_{GES}	Gate-Emitter Voltage		± 20	V	
	Transient Gate-Emitter Voltage	± 25	V		
I _C	Continuous Collector Current	$T_{\rm C}$ = 80 °C, $T_{\rm Jmax}$ = 175 °C	40	А	
Ісм	Pulsed Collector Current	limited by T _{Jmax}	80	Α	
P_{D}	Maximum Power Dissipation		156	W	
TJ	Operating Junction Temperature		- 40 to + 150	°C	
Protection I	Diode				
V _{RRM}	Peak Repetitive Reverse Voltage		650	V	
I _F	Continuous Forward Current	T _C = 80 °C, T _{Jmax} = 175 °C	15	Α	
I _{FM}	Maximum Forward Current		30	Α	
I _{FSM}	Non-repetitive Peak Surge Current	60Hz Single Half-Sine Wave	150	Α	
l ² t - value	Surge Current Integral Value		93	A ² s	
P _D	Maximum Power Dissipation		140	W	
TJ	Operating Junction Temperature		- 40 to + 150	°C	
Boost Diod	e				
V _{RRM}	Peak Repetitive Reverse Voltage		650	V	
I _F	Continuous Forward Current	T _C = 80 °C, T _{Jmax} = 175 °C	15	Α	
I _{FM}	Maximum Forward Current		30	Α	
I _{FSM}	Non-repetitive Peak Surge Current	60Hz Single Half-Sine Wave	120	Α	
l ² t - value	Surge Current Integral Value		60	A ² s	
P_{D}	Maximum Power Dissipation		98	W	
T _J	Operating Junction Temperature		- 40 to + 150	°C	
Module					
T _{STG}	Storage Temperature		- 40 to + 125	°C	
V _{ISO}	Isolation Voltage	AC 1 min.	2500	V	
IsoMaterial			Al_2O_3	-	
T _{MOUNT}	Mounting Torque		2.0 to 5.0	N•m	
Creepage	Terminal to Heat Sink	11.5	mm		
	Terminal to Terminal	6.3	mm		
Clearance	Terminal to Heat Sink	10.0	mm		
	Terminal to Terminal	5.0	mm		

Electrical Characteristics $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Boost IGI	ЗТ					
Off Charac	teristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0 V, I _C = 1 mA	650	-	-	V
I _{CES}	Collector Cut-off Current	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μΑ
I _{GES}	Gate-Emitter Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0 V	-	-	± 2	μΑ
On Charac	teristics					
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 40 \text{ mA}$	3.9	5.1	6.8	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 40 \text{ A}, V_{GE} = 15 \text{ V}$	-	1.55	2.2	V
		I_C = 40 A, V_{GE} = 15 V, T_C = 125 °C	-	1.85	-	V
R_{LEAD}	Lead Resistance of Pin to Chip	per Chip	-	3.3	-	mΩ
Switching	Characteristics					
$t_{d(on)}$	Turn-On Delay Time	V _{CC} = 300 V	-	24	-	ns
t _r	Rise Time	$I_{C} = 40 \text{ A}$	-	24	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GE} = 15 \text{ V}$ $R_{G} = 15 \Omega$	-	132	-	ns
t _f	Fall Time	Inductive Load	-	17	-	ns
E _{ON}	Turn-On Switching Loss per Pulse	T _C = 25 °C	-	0.40	-	mJ
E _{OFF}	Turn-Off Switching Loss per Pulse		-	0.28	-	mJ
t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V	-	22	-	ns
t _r	Rise Time	I _C = 40 A	-	27	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{GE} = 15 V	_	148	-	ns
t _f	Fall Time	$R_G = 15 \Omega$ Inductive Load	-	17	-	ns
E _{ON}	Turn-On Switching Loss per Pulse T _C = 125 °C		-	0.59	-	mJ
E _{OFF}	Turn-Off Switching Loss per Pulse		-	0.37	-	mJ
Qq	Total Gate Charge	V _{CC} = 300 V, I _C = 40 A, V _{GE} = 15 V	-	65	-	nC
R _{θJC}	Thermal Resistance of Junction to Case	per Chip	-	-	0.96	°C/W
R _{θCH}	Thermal Resistance of Case to Heat sink	per Chip, λ _{PCM} = 3.4 W/mK	-	0.54	-	°C/W
Protectio		17 1 GW				
		Ι – 15 Λ		1.05	1.1	V
V_{F}	Diode Forward Voltage	I _F = 15 A I _F = 15 A, T _C = 125 °C	-		1.4	V
<u> </u>	Land Davistanas of Bircha Ohir		-	0.95	-	-
R _{LEAD}	Lead Resistance of Pin to Chip	per Chip	-	2.4	-	mΩ
I _R	Reverse Leakage Current	V _R = 650 V	-	-	250	μΑ
R _{θJC}	Thermal Resistance of Junction to Case	per Chip	-	-	1.07	°C/W
R _{θCH}	Thermal Resistance of Case to Heat sink	per Chip, λ_{PCM} = 3.4 W/mK	-	0.33	-	°C/W
Boost Did	ode					
V _F	Diode Forward Voltage	I _F = 15 A	-	1.45	1.9	V
		I _F = 15 A, T _C = 125 °C	-	1.75	-	V
R _{LEAD}	Lead Resistance of Pin to Chip	per Chip	-	2.8	-	mΩ
I _R	Reverse Leakage Current	V _R = 650 V	-	//-	60	μΑ
I _{rr}	Reverse Recovery Current $V_R = 300 \text{ V}, I_F = 15 \text{ A},$		-	9.2	-	Α
Q _C	Total Capacitive Charge	di / dt = 1390 A/us,	-	60	-	nC
E _{rec}	Reverse Recovery Energy	T _C = 25 °C	-	4.9	-	μJ
I _{rr}	Reverse Recovery Current	V _R = 300 V, I _F = 15 A,	-	9.2	-	Α
Q _C	Total Capacitive Charge	di / dt = 1390 A/us,	-	65	-	nC
E _{rec}	Reverse Recovery Energy	T _C = 125 °C	-	4.9	-	μJ
R _θ JC	Thermal Resistance of Junction to Case	per Chip	-	-	1.52	°C/W
R _{0CH}	Thermal Resistance of Case to Heat sink	per Chip, λ_{PCM} = 3.4 W/mK	_	0.18	_	°C/W

Electrical Characteristics $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
NTC (The	rmistor)	•			•	
R _{NTC}	Rated Resistance	T _C = 25 °C	-	10	-	kΩ
		T _C = 100 °C	-	936	-	Ω
	Tolerance	T _C = 25 °C	- 3	-	+ 3	%
P_{D}	Power Dissipation	T _C = 25 °C	-	-	20	mW
B _{Value}	B-Constant	B _{25/50}	-	3450	-	K
		B _{25/100}	-	3513	-	K

Typical Performance Characteristics

Fig 1. Typical Output Characteristics



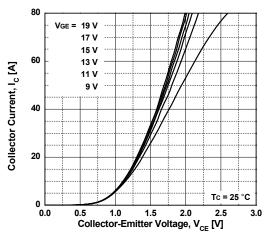


Fig 2. Typical Output Characteristics

- IGBT

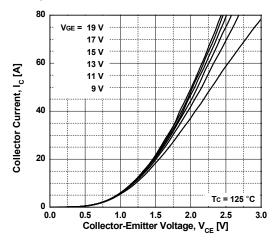


Fig 3. Typical Saturation Voltage Characteristics - IGBT

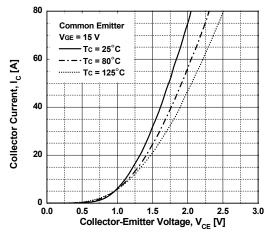


Fig 4. Switching Loss vs. Collector Current

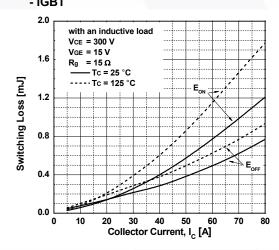


Fig 5. Switching Loss vs. Gate Resistance - IGBT

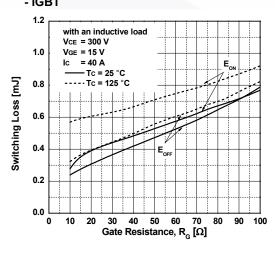
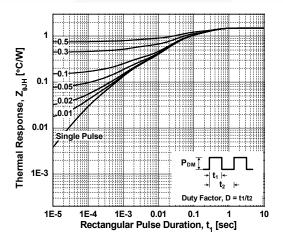


Fig 6. Transient Thermal Impedance - IGBT



Typical Performance Characteristic

Fig 7. Typical Forward Voltage Drop

- Protection Diode

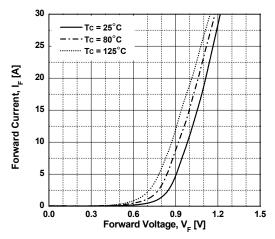


Fig 8. Transient Thermal Impedance
- Protection Diode

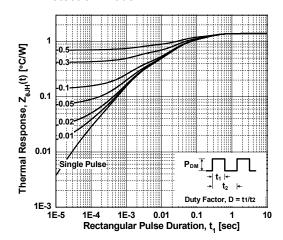


Fig 9. Typical Forward Voltage Drop

- Boost Diode

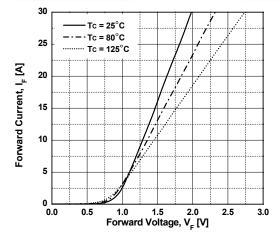


Fig 10. Reverse Recovery Energy vs. Forward Current

- Boost Diode

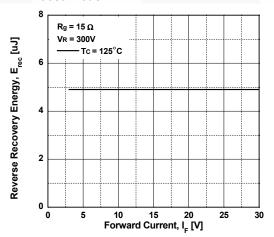
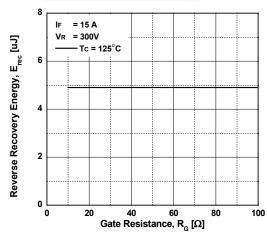
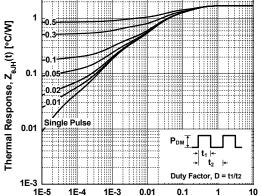


Fig 11. Reverse Recovery Energy vs. Gate Resistance Fig 12. Transient Thermal Impedance

- Boost Diode

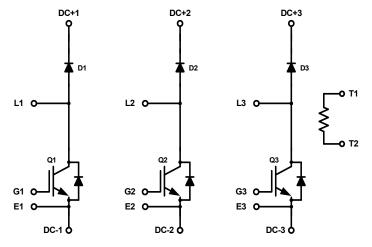




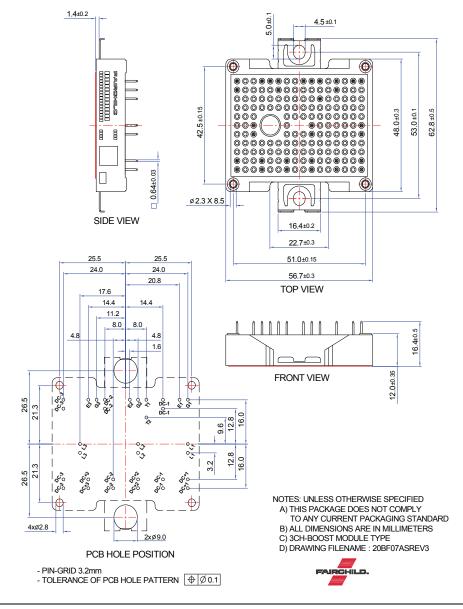
1E-4 1E-3 0.01 0.1 1 Rectangular Pulse Duration, $\mathbf{t_1}$ [sec]

- Boost Diode

Internal Circuit Diagram



Package Outlines [mm]







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