



N-Channel 30-V (D-S) MOSFET

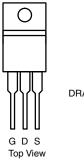
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$ $I_{D}(A)^{a, e}$		Q _g (Typ)		
30	0.0029 at V _{GS} = 10 V	90	82 nC		
30	0.0033 at V _{GS} = 4.5 V	90	02 110		

FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2011/65/EU



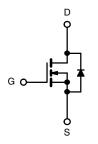
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APPLICATIONS

- OR-ing
- Server
- DC/DC



N-Channel MOSFET

Ordering	Information:	SUP90N03-03-F3	(Lead (Ph)-free)

ABSOLUTE MAXIMUM RATINGS	(T _A = 25 °C, unle	ess otherwise no	oted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	± 20	V
	T _C = 25 °C		90 ^{a, e}	
Continuous Drain Current (T _{.1} = 175 °C)	T _C = 70 °C	l , [90 ^e	
Continuous Diain Current (1) = 175 C)	T _A = 25 °C	l _D –	28.8 ^{b, c}	Α Α
	T _A = 70 °C		27 ^{b, c}	^
Pulsed Drain Current		I _{DM}	90	
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	36	
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	64.8	V
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	90 ^{a, e}	A
Continuous Source-Drain Diode Current	T _A = 25 °C	'S	3.13 ^{b, c}	A
	T _C = 25 °C		250 ^a	
Maximum Dawar Dissination	T _C = 70 °C	P _D	175	w
Maximum Power Dissipation	T _A = 25 °C	r _D	3.75 ^{b, c}	VV
	T _A = 70 °C		2.63 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Тур.	Max.	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R _{thJA}	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R_{thJC}	0.5	0.6	O/ V V	

Notes:

- a. Based on T_C = 25 °C. b. Surface mounted on 1" x 1" FR4 board.

- b. Suitace mounted on 1 X 1 114 states.
 c. t = 10 sec.
 d. Maximum under steady state conditions is 90 °C/W.
 e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

SUP90N03-03

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SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	ι _D = 230 μΑ		- 7.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oata Walkana Busin Ouwant	_	V _{DS} = 30 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α	
		$V_{GS} = 10 \text{ V}, I_D = 28.8 \text{ A}$		0.0024	0.0029		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 27 \text{ A}$		0.0027	0.0033	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 28.8 A		160		S	
Dynamic ^b			I.	•		ı	
Input Capacitance	C _{iss}			12065			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1725		pF	
Reverse Transfer Capacitance	C _{rss}			970			
Total Oaks Observe		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 28.8 \text{ A}$		171	257		
Total Gate Charge	Qg		1	81.5	123	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 28.8 \text{ A}$		34			
Gate-Drain Charge	Q_{gd}			29			
Gate Resistance	R_{g}	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t _{d(on)}			18	27		
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		11	17		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 24$ A, $V_{GEN}=10$ V, $R_g=1$ Ω		70	105		
Fall Time	t _f			10	15		
Turn-On Delay Time	t _{d(on)}			55	83	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.67 Ω		180	270		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 22.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		55	83		
Fall Time	t _f	-		12	18		
Drain-Source Body Diode Characteristic	S			ı			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			90		
Pulse Diode Forward Current ^a	I _{SM}				90	Α	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	00 A 31/41 400 A/ T 67 00		70.2	105	nC	
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27			
Reverse Recovery Rise Time	t _b			25		ns	

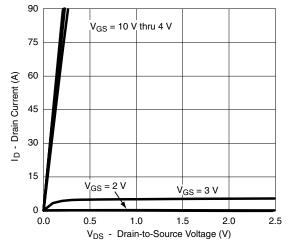
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

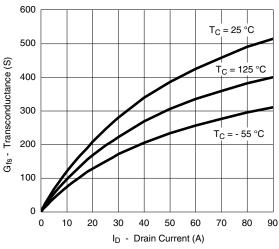
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



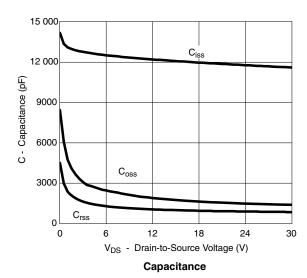
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Output Characteristics

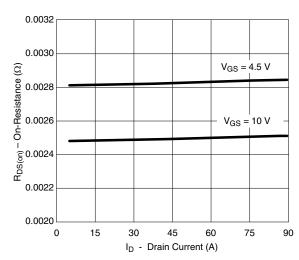


Transconductance

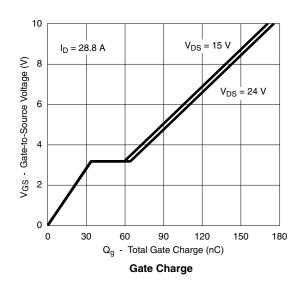


3.0 2.4 I_D - Drain Current (A) 1.8 1.2 T_C = 25,°C 0.6 T_C = 125 °C Γ_C = - 55 °C 0.0 V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics

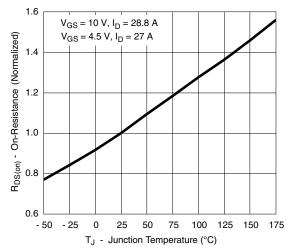


R_{DS(on)} vs. Drain Current

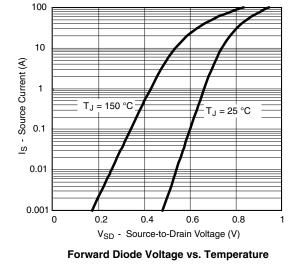


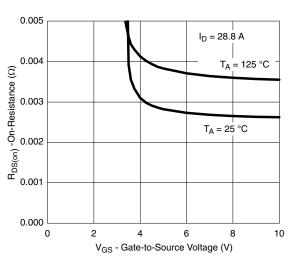
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

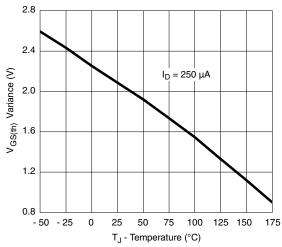


On-Resistance vs. Junction Temperature

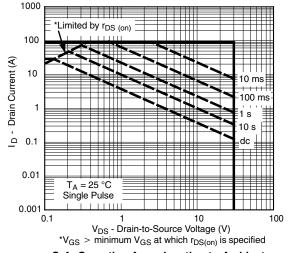




 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



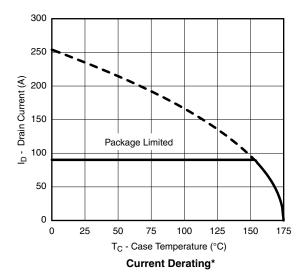
Threshold Voltage

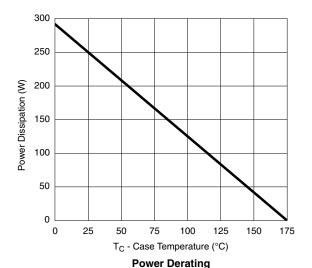


Safe Operating Area, Junction-to-Ambient

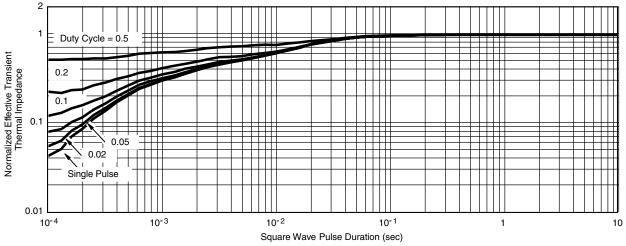


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



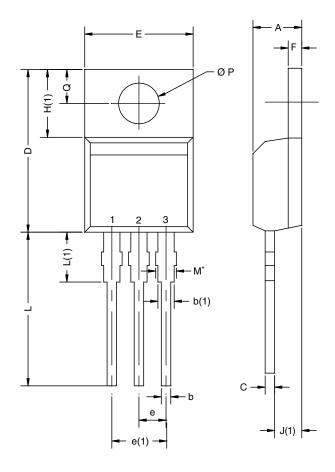
Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?74341





TO-220AB



	D2

	MILLIMETERS		INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
D2	12.19	12.70	0.480	0.500	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471					

Note

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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