HZ0805B272R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

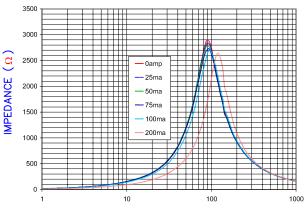
A 2.00 [.079] ± 0.20 [.008]

B 1.25 [0.049] ± 0.20 [.008]

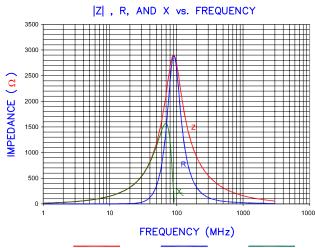
C 1.25 [.049] ± 0.20 [.008]

D 0.51 $[.020] \pm 0.25 [.010]$

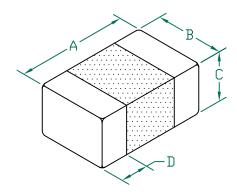
Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)



AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3255



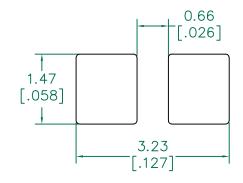


ELECTRICAL CHARACTERISTICS:										
Z @ 100M (Ω)	1Hz	DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current							
Nominal	2700									
Minimum	2025									
Maximum	3375	0.8	200 mA							

NOTES: UNLESS OTHERWISE SPECIFIED

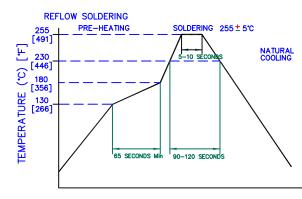
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL, PLASTIC TAPE
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH 100% TIN.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS





	DIMENSIONS ARE IN mm [INCHES].			This print is the property of Lair	d						
				Tech, and is loaned in confidence subject to return upon request a		Laird					
				with the understanding that no							
				copies shall be made without the written consent of Laird Tech. All		Laliu					
E	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	rights to design or invention are reserved.							
В	UPDATE COMPANY LOGO ADD ROHS SYMBOL	03/19/08	JRK	PROJECT/PART NUMBER:	REV	PAR	T TYPE:	DRAWN BY:			
С	REVISE DIMENSION C	02/16/06	JRK	HZ0805B272R-10		_ 0	O-FIRE	JRK			
В	CORRECT TYPO ERROR ON DIMS B	11/30/05	JRK	DATE: 04/16/04	CALE;	NTS	SHEET:				
Α	ORIGINAL DRAFT	04/16/04	JRK		TOOL #	NIS		of 2			
REV	DESCRIPTION	DATE	INT	THZ0805B272R-10-E		_		01 2			